**K22U 1511**

Reg. No. : ...................................

Name : ...................................

IV Semester B.C.A. Degree CBCSS (OBE) Regular/ Supplementary/
Improvement Examination, April 2022
(2019 Admission Onwards)
Core Course
**4B09BCA : COMPUTER ORGANIZATION**

Time : 3 Hours

Max. Marks : 40

## PART – A
### (Short Answer)

Answer **all** questions.

(6×1=6)

1. What is the purpose of a programming language ?

2. Convert the expression (A+B)·C to RPN.

3. What is parallel processing ?

4. Define hit ratio.

5. Define Content Addressable Memory.

6. Expand VLSI.

## PART – B
### (Short Essay)

Answer **any 6** questions.

(6×2=12)

7. Define a three state gate.

8. What is the purpose of BUN instruction ?

9. Write note on synchronous and asynchronous serial transmission.

10. Explain the necessity of DMA.

11. List the address sequence capabilities required in control memory.

12. What are the physical forms available for establishing an interconnection network ?

13. Differentiate RAM and ROM.

14. Write note on virtual memory.

P.T.O.

## PART – C
### (Essay)

Answer **any 4** questions. (4×3=12)

15. Write note on stored program organization.

16. Explain about conditional branching with diagram.

17. Explain in detail about instruction pipeline.

18. Write note on daisy chaining priority interrupt.

19. Explain about the role of crossbar switch in interconnection structures.

20. Explain about register stack organization.

## PART – D
### (Long Essay)

Answer **any 2** questions. (2×5=10)

21. Explain in detail about instruction formats.

22. Explain in detail about the different types of addressing modes.

23. Explain about the cache memory mapping techniques.

24. Explain the techniques used in Asynchronous data transfer.

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